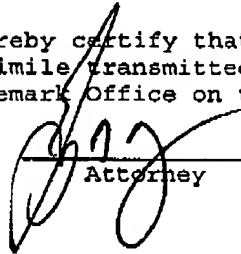


GR 00 P 4121

CERTIFICATION OF FACSIMILE TRANSMISSION

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DateIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 09/871,013 Confirmation No:8219  
Applicant : Albert Birner et al.  
Title : Method For Making Electrical Contact With a  
Rear Side Of a Semiconductor Substrate During  
Its Processing  
Filed : May 31, 2001  
Art Unit : 2823  
Examiner : William D. Coleman  
Docket No. : GR 00 P 4121  
Customer No.: 24131

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Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**OFFICIAL**

S i r :

Responsive to the Office Action dated August 5, 2003, kindly amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.